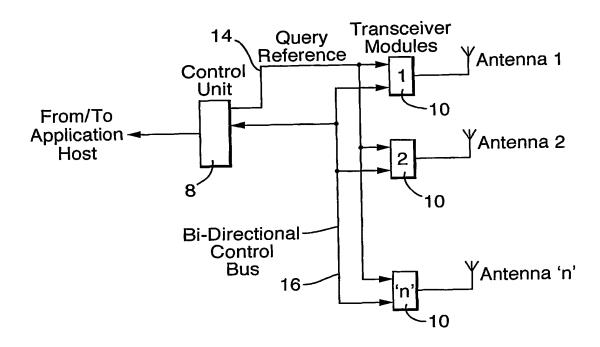
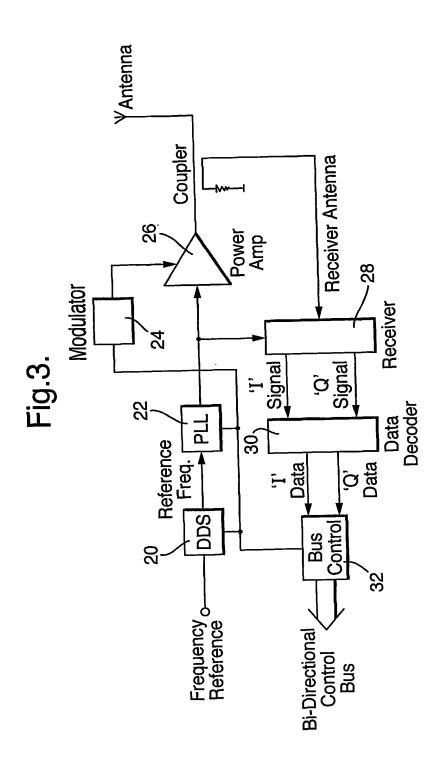


Fig.2.





DDS = Direct Digital Synthesiser
PLL = Phase Lock Loop Frequency Synthesiser
'I' = In-Phase

'Q' = Quadrature